IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Atty. Docket No.: 002372.00046

Koji HOSONO et al.

Serial No.: Divisional of 09/800,913, filed March

8, 2001

Filed: Herewith

For: NON-VOLATILE SEMICONDUCTOR

MEMORY

Group Art Unit: Unknown

Examiner: Unknown

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with Applicants' duty of disclosure, the following information is submitted for consideration by the United States Patent and Trademark Office in connection with the above-captioned application. The information is identified on the attached PTO-1449 form.

This application relies, under 35 U.S.C. § 120, on the earliest filing date of prior U.S. patent application serial no. 09/800,913, filed March 8, 2001. The documents identified on the attached PTO 1449 form were submitted to and/or cited by the Office in a prior application and, therefore, copies are not required to be provided in this application. (See 37 C.F.R. § 1.98(d)).

Applicants do not waive any right to take appropriate action to establish patentability over the listed documents should they be applied as references against the claims of the present application.

It is respectfully requested that the Examiner fully consider each of the documents, initial the enclosed Form PTO-1449 in the appropriate place to indicate that the document has been considered, and return a copy of the initialed form to the undersigned in accordance with MPEP Section 609.

Applicants believe that no fee is necessary pursuant to 37 C.F.R. § 1.97(b). However, if a fee is due, the Office is authorized to charge Deposit Account No. 19-0733.

Respectfully submitted,

BANNER & WITCOFF, LTD.

Dated: September 22, 2003

By:

Gary D. Hedorochko Registration No. 35,509

1001 G Street, N.W. Washington, D.C. 20001-4597 (202) 508-9100

GDF:lab

Please type a plus sign (+) inside this box	+
---	---

Sheet

of 1

Approved for use through 10/31/2002. OMB 0651-0031 U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

002372.00046

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Attorney Docket Number

Substitute for form 1449A/PTO		Complete if Known
	Application Number	TBA
INFORMATION DISCLOSURE	Filing Date	Herewith
STATEMENT BY APPLICANT	First Named Inventor	Koji HOSONO et al.
	Group Art Unit	Unknown
(use as many sheets as necessary)	Examiner Name	Unknown

			U.S. PATENT DOCUM	MENTS	
Examiner Cite No.1	U.S. Patent Document	Name of Patentee or Applicant	Date of Publication of	Pages, Columns, Lines, Where Relevan	
		Number Kind Code ² (if known)	of Cited Document	Cited Document MM-DD-YYYY	Passages or Relevant Figures Appear
		4,547,685	Wong	10-15-1985	
		5,521,869	Ishimura et al.	05-28-1996	
		5,701,268	Lee et al.	12-23-1997	
		5,751,634	Itoh	05-12-1998	
	Ī	5,966,326	Park et al.	10-12-1999	
		5,986,929	Sugiura et al.	11-16-1999	
		6,545,909	Tanaka et al.	04-2003	
		5,903,495	Takeuchi et al.	05-1999	
		5,163,021	Mehrotra		
		5,602,789	Endoh		
	<u> </u>				
	ļ				

				FOREIG	N PATENT DOCU	MENTS		
Examiner Initials*	Cite No. ¹	For Office ³	reign Patent Docu Number ⁴	ument Kind Code ⁵ (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ₆
		wo	95/34074	Α	Intel	12-14-1995	riguico rippodi	
		JP	57-152586	3 A	Nippon Denki	09-20-1982		Abst
		JP	57-19189) A	Nippon Denki	11-25-1982		Abst
		JP	10-011982	2	Hiromi	01-1998		Abst
		JP	10-092186	3	Masataka et al.	04-1998		Abst

Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Takeuchi et al., "Multipage Cell Architecture for High-Speed Programming Multilevel NAND Flash Memories," <i>IEEE J. Solid-State Circuits</i> , Vo. 33, No. 8, Aug. 1998, pp. 1228-1238	

Examiner Signature	Date Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.